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Facsimile Number: 571-273-8300	Transmission Date: February 19, 2007

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Tu	Docket No.:	TSM03-0188
Serial No.:	10/679,098	Patent No.:	6,853,024 B1
Date Filed:	October 3, 2003	Issue Date:	February 8, 2005
Title:	Self-Aligned MIM Capacitor Process for Embedded DRAM		

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Application Number	10/679,098
Filing Date	10/03/2003
First Named Inventor	Tu
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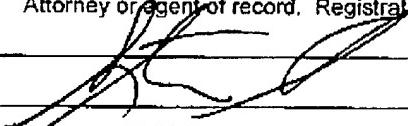
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